

## Prerna Srivastava

8-321 Indiranagar, near Sushma Hospital

Faizabad Road, Lucknow- 226016

Uttar Pradesh

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### Overview:

- Looking for an opportunity to secure the position of a teacher where I can employ all my skills for the benefit of the college. Well-skilled, educated followed by internship likely to achieve.
- Currently working as Guest Faculty in Electronics Department in Dr. Ambedkar Institute of Technology for Handicapped, Kanpur.
- Pursuing **Doctor of Philosophy (Ph.D.) in Electronics Engineering** with specialization in **VLSI-DSP** from **Harcourt Butler Technical University, Kanpur** from session 2022-23.
- Pursued **Master of technology (M.Tech) in Electronics and Communication Engineering** with specialization in **Communication Engineering** from **Kamla Nehru Institute of Technology, Sultanpur** affiliated to **Dr. A.P.J. Abdul Kalam Technical University** with an aggregate of **76.60%**.
- Pursued **Bachelor of technology (B.Tech) in Electronics and Communication Engineering** from **Shri Ramswaroop Memorial Group of Professional College, Lucknow** affiliated to **Dr. A.P.J. Abdul Kalam Technical University** with an aggregate of **72.02%**.

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### Professional Qualification

S.No.	Class	Board	Institute	Year	Percentage
1.	M.Tech	AKTU	KNIT, Sultanpur	2018	76.60
2.	B.Tech	AKTU	SRMGPC, Tewariganj, Lucknow	2016	72.02

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### Academic Credentials

S.No.	Class	Board	Institute	Year	Percentage
1.	XII	ISC	LPC, Sahara States, Jankipuram, Lucknow	2012	69.83
2.	X	ICSE	LPC, Sahara States, Jankipuram, Lucknow	2010	80.28

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### Technical skills

- VHDL
- Basics of C & JAVA
- MS-Powerpoint
- MS-Word

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### Area of Interest

- Digital Electronics
- Data Structures (basics)
- Communication Systems

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### Personal Skills

- Honest, hard-working, a quick learner and a good listener
- Ability to cope with the changing environment
- Possess positive attitude
- Responsible towards work

- Self- motivated and directed

## **Training**

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**Name of Organization:** Uttar Pradesh Power Corporation Limited, Lucknow

**Title of Project:** Communication in Power Grid System for Electricity Distribution

**Duration:** 4 Weeks (15/06/2015-14/07/2015)

## **Academic Project**

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- **B.Tech :**

**Name of the College:** SRMGPC, Lucknow

**Title of Project:** A Prime Factor FFT Algorithm Using High Speed Convolution

**Duration:** 9 Months (01/08/2015-01/05/2016)

- **M.Tech :**

**Name of the College:** KNIT, Sultanpur

**Title of Project:** Design of 16-bit ALU with Booths Multiplier Using Radix-4 and Its FPGA Implementation

**Duration:** 10 Months (01/08/2017-01/06/2018)

## **Conference and Journal Published**

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- Prerna Srivastava and Prof. Rakesh Kumar Singh, “Design of 16-bit ALU with Booths Multiplier Using Radix-4 and Its FPGA Implementation”, Proceedings of International Conference on Sustainable Computing in Science, Technology and Management, Elsevier SSRN, SSRN Id- 3355190, pg. 858-869, February, 2019.
- Prerna Srivastava and Prof. Rakesh Kumar Singh, “Design of 16-bit ALU with Booths Multiplier Using Radix-4 and Its FPGA Implementation”, The IUP Journal of Electrical & Electronics Engineering, vol.XIII no. 4, pp. 30-44, October, 2020.
- Prerna Srivastava, Md. Kaif et.al. ,“A Review Paper on Rice Quality Analysis Using Image Processing Technique”, International Journal for Research in Applied Science & Engineering Technology (IJRASET), vol. 10 issue V, pp. 292-294, May, 2022.

## **Workshops Attended**

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- One week workshop on “Communication & Antenna Design for IOT (CADIT-2017)” at MNNIT, Allahabad. (22-27 September, 2017)
- One week workshop on “Antenna Design and Signal Processing Techniques for 5G Networks and IOT (ADSPNIT-2017)” organized at MNIIT, Allahabad. (27 February-4 March, 2017).

## **Hobbies**

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Listening to music, cooking, landscape sketching.

## **Personal Details**

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**Date of Birth:** 10-July-1996

**Mother’s Name:** Poonam Srivastava

**Father’s Name:** Prabhat Kumar Srivastava

**Nationality:** Indian

**Gender:** Female

**Languages Known:** English and Hindi

**Date:** 04/09/2022

**Place:** Lucknow