

# CURRICULUM VITAE

Bachchh Raj Singh  
DOB: 8 Jan1973

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## 1. Education:

1999-M.TECH (Microwave Engineering)  
Department of Electronics Engineering  
IIT-BHU Varanasi, U.P. India

1996-B.TECH  
Department of Electronics and Communication Engineering  
MNNIT Allahabad U.P India

## 2. Employment History

Total Teaching Experience: 23 Years

- i. (Sep 2021-Till date) as Assistant Professor EL in Dr. AITD, Kanpur.
- ii. (July 2013-May 2020) as Associate Professor& HOD (EC &EE) in BIET Lucknow.
- iii. (August2009-March2013) as Associate Professor& HOD in RIET, Kanpur.
- iv. August 2006-July 2009) as Associate Professor/Assistant Professor in PSIT Kanpur
- v. (August 2005-October 2006) as Assistant Professor in KIT, Kanpur.
- vi. (August2002-July2005) as Senior lecturer in SITM, Lucknow.
- vii. (Oct1999-October 2001) as Lecturer in UNSIET (VBSPU), Jaunpur .

## 3. Publications

- i. M. Premkumar, Vikash Sachan, B.R Singh, " Data Transmission and Reception in Spatial Modulation MIMO Wireless Systems and Analysis in Nakagami-m Fading channels"Traitement du Signal (SCIE),Vol. 40, No. 1, pp. 401-406, February, 2023.
- ii. Bachchh Raj Singh, Niraj Kumar, Vikash Sachan, Richa Verma, M.Premkumar, "Investigation of MIMO Cooperative Relaying Systems Employing Space Time Block Code",International Conference on Recent Advances in Emerging Computing and Communication Technologies (ICRAECCT-2025), 11-12 April 2025.

- iii. Vikash Sachan, Rohan Kumar, Riya Sachan, Arjun Jaiswal, Guddu Sahani, Shweta Tripathi, B R Singh, “Investigation of Pairwise Error Probability of  $\alpha$ - $\mu$  Fading Channel in Spatially modulated MIMO System”, 2nd IEEE International Conference on Emerging Frontiers in Electrical and Electronic Technologies (ICEFEET - 2022), on June 24-25, 2022.
- iv. Low power and high speed adder using Fin-FET Technology *ijiesm* volume 6 Issue 4 April 2018.
- v. An efficient routing algorithm to improve performance & to reduce Energy consumption in wireless sensor networks using BAT algorithm, *ijiesm*, Volume 4, Issue 3, March 2016).
- vi. Design of Floating Point Vedic Multiplier using VHDL ,*ijiesm*, Volume 4, Issue 2, February 2016.
- vii. “A Review of Latest Researches in Optical Communication” in International Conference on Emerging Technologies and Applications in Engineering, Technology and Sciences (ICETAETS 2008), India.
- viii. M-Health: “A New Research Trend For Health Professionals” in International Conference on Emerging Technologies and Applications in Engineering, Technology and Sciences (ICETAETS2008), India

#### 4. Course, Workshops/FDP

- i. Attended FD on “Electric Vehicles: Issues and Challenges” from 2<sup>nd</sup> to 6<sup>th</sup> June 2025 Organised by GBPIET Uttarakhand India.
- ii. Attended FDP on “Assistive Technology for Equitable and Inclusive Education” from 23<sup>rd</sup> to 27<sup>th</sup> July 2024 Organised by Dr. AITD Kanpur.
- iii. Attended FDP on **“Design and Implementation issues in 5G/6G Wireless Communication Networks”** from 20<sup>th</sup> to 25<sup>th</sup> December 2023 Organised by Rajkiya Engineering College Kannauj.
- iv. Attend short term training Programme on Indian Electricity Rule and Code of Practices from 16 to 20 December, 2019 conducted by NITTTR Kolkata.
- v. Two week ISTE Workshop “Control System” conducted by IIT Kharagpur from 2<sup>nd</sup> to 12<sup>th</sup> December, 2014 through ICT (MHRD).
- vi. Two-week ISTE Workshop on “Signal & System” conducted by IIT Kharagpur from 2<sup>nd</sup> to 12<sup>th</sup> January, 2014 through ICT(MHRD).
- vii. Completed “MISSIOM10X” Program by “Wipro”.(23-25 March 2012)
- viii. Attended Dale Carnegie “High Impact Teaching Skills” workshop at RIET Kanpur.(26-27 March 2012) by “Wipro.
- ix. Certification course “Smart Electric Grid Solutions “from IIT Kanpur Through QIP (AICTE), (15-19 Nov 2010).

## **5. Thesis and Projects**

- i. B. R Singh, Analysis of Square Patch with  $90^\circ$  hybrid feed system for circular polarization, M.Tech. Thesis, IIT BHU Varanasi, UP, 1999.
- ii. B.R. Singh, F.S.K Modem and interfacing with PC for Data transmission, B.Tech project, M.N.R.E.C Allahabad , U.P, 1996.

**6. Membership:** IEI (Institution of Engineers, India)

**7. Soft Skills:** MS Office, MATLAB, VHDL.

**Date:**

**Place**

**B. R. Singh**